

(Established under The Tripura Act No. 4 of 2023 & UGC u/s 2(f) of UGC Act. 1956)

आनंदनगर, महेशखोला, त्रिपुरा (पश्चिम) -799004/ Anandanagar, Maheshkhola, Agartala, Tripura (West)
ई / मेल-e-Mail: coe@tiut.ac.in, वेबसाइट / Website: www.tiut.ac.in, टेलीफोन/Phone No.: (+91)9230994083

Ref. No. TIUT/COE/05/Lab-Exam/Notice/ 62/ 2025-26/1401

NOTIFICATION

Maheshkhola, Anandanagar, Dated: 31.12.2025

This is to notify all concerned students that the **Practical Examinations** of the **Department of Computer Science & Engineering** for the concerned **UG programmes** will be conducted as per the schedule given below.

| EXAM | SEMESTER | BRANCH | DATE |
|--|----------|--------|-----------|
| Engineering Physics Lab 1 | I | AIML | 5/1/2026 |
| Mathematical Concepts of AI Lab | I | AIML | 7/1/2026 |
| Engineering Chemistry Lab 1 | I | AIML | 6/1/2026 |
| Engineering Graphics & Design Lab | I | AIML | 8/1/2026 |
| Programming for Problem Solving Lab | I | AIML | 9/1/2026 |
| Engineering Physics Lab 1 | I | CORE | 6/1/2026 |
| Basic Electrical Engineering I Lab | I | CORE | 7/1/2026 |
| Engineering Chemistry Lab 1 | I | CORE | 5/1/2026 |
| Engineering Graphics & Design Lab | I | CORE | 12/1/2026 |
| Programming for Problem Solving Lab | I | CORE | 9/1/2026 |
| Engineering Physics Lab 1 | I | IoT | 6/1/2026 |
| Digital Electronics Lab | I | IoT | 8/1/2026 |
| Engineering Chemistry Lab 1 | I | IoT | 5/1/2026 |
| Engineering Graphics & Design Lab | I | IoT | 12/1/2026 |
| Programming for Problem Solving Lab | I | IoT | 9/1/2026 |
| IT Workshop Lab | III | AIML | 5/1/2026 |
| Data Structure and Algorithms Lab | III | AIML | 7/1/2026 |
| Machine Learning Lab | III | AIML | 12/1/2026 |
| IT Workshop Lab | III | CORE | 6/1/2026 |
| Data Structure and Algorithms Lab | III | CORE | 8/1/2026 |
| Digital Electronics Lab | III | CORE | 12/1/2026 |
| IT Workshop Lab | III | IoT | 6/1/2026 |
| Data Structure and Algorithms Lab | III | IoT | 7/1/2026 |
| Computer Organization & Architecture Lab | III | IoT | 8/1/2026 |
| Microprocessor Lab | III | IoT | 13/1/26 |
| Computer Networks Lab | V | CORE | 12/1/2026 |
| Software Engineering Lab | V | CORE | 13/1/2026 |
| Machine Learning Lab | V | CORE | 15/1/2026 |

Students are advised to note the following instructions carefully:

- Students must report to the examination venue **at least 30 minutes before** the scheduled time.
- Carry **University Identity Card** and other required documents without fail.

City Office:

Tripura Darpan Building (Above FabIndia Store), 2nd Floor, Cornel Chowmuhani, Agartala- 799001, West Tripura, Tripura, India

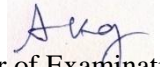
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- Practical files, records, and other required materials must be duly completed and certified.
- Any form of **unfair means** during the examination will be dealt with as per university rules.
- Attendance in the practical examination is **mandatory**.

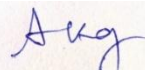
All students are directed to strictly adhere to the instructions issued by the University.

This notification has been issued with the prior approval of Hon'ble Vice-Chancellor.


Controller of Examinations (i/c)

Copy to:

1. PA to VC for kind information to Hon'ble VC, TIUT.
1. PA to Dean (Acad.) for kind information to Dean of Acad. Affairs.
2. Director, IQAC for kind information.
3. HoD / In-Charge of the Department of Computer Application.
4. Registrar (i/c) for information and needful action.
5. In-Charge, University official Website for upload it on website.
6. Notice Board.
7. Guard File.


DR. ABIR KUMAR GORAI
Controller of Examinations (I/C)
Techno India University, Tripura